UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO. : 7,012,831 B2 APPLICATION NO.: 10/749559

: March 14, 2006

DATED INVENTOR(S) : Kenichi Shigenami

> It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 12:

Line 55, "supplies" should read -- supplies the signal --.

Line 58, "two bit" should read -- two bit lines is provided to each column and two bit line of the --.

Line 67, "column" should read -- columns--.

Signed and Sealed this

Page 1 of 1

Thirteenth Day of February, 2007

JON W. DUDAS Director of the United States Patent and Trademark Office